

REMARKS/ARGUMENTS

Claims 21-26 are pending in the application with claims 21 and 26 amended; reexamination and reconsideration are hereby requested.

1. Claims 21-23 and 25-26 were rejected as unpatentable over admitted prior art application Fig.2 in view of Fujioka. The Examiner considered the substitution of bias voltage generator 61-66 of Fujioka Fig.1 for V_{BIAS} in application Fig.2 with transistors 122, 222 as the required common-mode transistors.

Applicant replies that prior art Fig.2 has common-mode feedback for each stage by transistors 132, 232 and that transistors 122, 222 are current sources, not common-mode adjustments; see application page 7, line 21 through page 8, line 16. Claims 21 and 26 have been amended to clarify that the common-mode transistor relates to the loads (resistors 129-131, 229-231 in Fig.2) and not the current source of each differential pair. Consequently, the amended claims are not suggested by the references.

Respectfully submitted,

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